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2 AMENDMENTS TO THE CLAIMS

3 Please amend the claims of the present application as set forth below. In
4 accordance with the PTO's revised amendment format, a detailed listing of all claims has
5 been provided. This listing of claims will replace all prior versions, and listings, of
6 claims in the application. Changes to the claims are shown by strikethrough (for deleted
7 matter) and underlining (for added matter).

8 By way of overview: a) Claims 2-3, 5, 9-22, 24-27, 31-43, and 45 remain in their
9 original form, without amendment; b) Claims 1, 4, 6-8, 23, 28-30, and 44 are currently
10 amended; and c) Claims 46-61 are added herein.

11
12 Listing of Claims

13 ~~1. (Currently Amended) A video output system for producing video signals within~~
14 ~~a video graphics workstation, the video output system comprising:~~

15 ~~a receiver for receiving a video signal forwarded from a video signal source~~
16 ~~within the video graphics workstation;~~

17 ~~a video pipeline for post-processing the received video signal, the video pipeline~~
18 ~~producing a post-processed video signal; and~~

19 ~~a video output module for converting the post-processed video signal, the video~~
20 ~~output module producing a formatted video signal.~~

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22 2. (Original) The video output system according to claim 1 wherein the video
23 output module further comprises:

24 an ancillary data injector, the injector inserting ancillary data into the
25 post-processed video signal.

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2 3. (Original) The video output system according to claim 1, further comprising:
3 a generator locking device.
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5 4. (Currently Amended) The video output system according to claim 1 wherein
6 the video ~~input~~ output module includes a generator locking device.
7

8 5. (Original) The video output system according to claim 1 wherein the received
9 video signal is e-VS, wherein e-VS is an RGB encoded video signal, an RGBA encoded
10 video signal, a YUV-Type encoded video signal, or a YUVA-Type encoded video signal.
11

12 6. (Currently Amended) The video output system according to claim 1 wherein
13 ~~the received video signal is forwarded from~~ the video signal source is a storage medium
14 that stores data in electrical form.
15

16 7. (Currently Amended) The video output system according to claim 1 wherein
17 ~~the received video signal is forwarded from~~ the video signal source is a video graphics
18 processor.
19

20 8. (Currently Amended) The video output system according to claim 1 wherein
21 ~~the received video signal is forwarded from~~ the video signal source is a video signal input
22 system.
23

24 9. (Original) The video output system according to claim 1 wherein the formatted
25 video signal is VS, wherein VS is an analog composite video signal, an analog

1 component video signal, a serial digital composite video signal, a serial digital
2 component video signal, a parallel digital composite video signal, or a parallel digital
3 component video signal.

4
5 10. (Original) The video output system according to claim 1 wherein the process
6 of post-processing includes region of interest selection.

7
8 11. (Original) The video output system according to claim 1 wherein the process
9 of post-processing includes frame rate matching.

10
11 12. (Original) The video output system according to claim 1 wherein the process
12 of post-processing includes spatial adaptation.

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14 13. (Original) The video output system according to claim 12 wherein the process
15 of spatial adaptation includes scaling.

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17 14. (Original) The video output system according to claim 12 wherein the process
18 of spatial adaptation includes picture framing.

19
20 15. (Original) The video output system according to claim 14 wherein the process
21 of picture framing includes letter boxing.

22
23 16. (Original) The video output system according to claim 1 wherein the process
24 of post-processing includes changing the sample rate of the video signal being
25 post-processed.

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2 17. (Original) The video output system according to claim 1 wherein the process
3 of post-processing includes gamma removal.

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5 18. (Original) The video output system according to claim 1 wherein the process
6 of post-processing includes gamma insertion.

7
8 19. (Original) The video output system according to claim 1 wherein the process
9 of post-processing includes color space conversion.

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11 20. (Original) The video output system according to claim 1 wherein the process
12 of post-processing includes changing frames of video data into interleaved fields of video
13 data.

14
15 21. (Original) The video output system according to claim 1 wherein the process
16 of post-processing includes addressing on a frame-by-frame basis the video signal being
17 post-processed.

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19 22. (Original) The video output system according to claim 1 wherein the system is
20 a Peripheral Component Interconnect circuit board.

21
22 23. (Currently Amended) A method for producing video signals using a video
23 output system within a video graphics workstation, the method comprising:

1 receiving a video signal in a receiver of the video output system, wherein the
2 video signal is forwarded from a video signal source within the video graphics
3 workstation,

4 post-processing the received video signal through a video pipeline of the video
5 output system, producing a post-processed video signal; and

6 converting the post-processed video signal in a video output module of the video
7 output system, producing a formatted video signal.

8
9 24. (Original) The method according to claim 23, further comprising:
10 inserting ancillary data into the post-processed video signal prior to converting the
11 post-processed video signal.

12
13 25. (Original) The method according to claim 23, further comprising:
14 generator locking the received video signal.

15
16 26. (Original) The method according to claim 23 wherein the video output module
17 includes a generator locking device.

18
19 27. (Original) The method according to claim 23 wherein the received video
20 signal is e-VS, wherein e-VS is an RGB encoded video signal, an RGBA encoded video
21 signal, a YUV-Type encoded video signal, or a YUVA-Type encoded video signal.

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23 28. (Currently Amended) The method according to claim 23 wherein ~~the received~~
24 ~~video signal is forwarded from~~ the video signal source is a storage medium that stores
25 data in electrical form.

1
2 29. (Currently Amended) The method according to claim 23 wherein ~~the received~~
3 ~~video signal is forwarded from~~ the video signal source is a video graphics processor.

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5 30. (Currently Amended) The method according to claim 23 wherein the ~~received~~
6 ~~video signal is forwarded from~~ the video signal source is a video signal input system.

7
8 31. (Original) The method according to claim 23 wherein the formatted video
9 signal is VS, wherein VS is an analog composite video signal, an analog component
10 video signal, a serial digital composite video signal, a serial digital component video
11 signal, a parallel digital composite video signal, or a parallel digital component video
12 signal.

13
14 32. (Original) The method according to claim 23 wherein the process of
15 post-processing includes region of interest selection.

16
17 33. (Original) The method according to claim 23 wherein the process of
18 post-processing includes frame rate matching.

19
20 34. (Original) The method according to claim 23 wherein the process of
21 post-processing includes spatial adaptation.

22
23 35. (Original) The method according to claim 34 wherein the process of spatial
24 adaptation includes scaling.
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1 36. (Original) The method according to claim 34 wherein the process of spatial
2 adaptation includes picture framing.

3
4 37. (Original) The method according to claim 36 wherein the process of picture
5 framing includes letter boxing.

6
7 38. (Original) The method according to claim 23 wherein the process of
8 post-processing includes changing the sample rate of the video signal being
9 post-processed.

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11 39. (Original) The method according to claim 23 wherein the process of
12 post-processing includes gamma removal.

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14 40. (Original) The method according to claim 23 wherein the process of
15 post-processing includes gamma insertion.

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17 41. (Original) The method according to claim 23 wherein the process of
18 post-processing includes color space conversion.

19
20 42. (Original) The method according to claim 23 wherein the process of
21 post-processing includes changing frames of video data into interleaved fields of video
22 data.

1 43. (Original) The method according to claim 23 wherein the process of
2 post-processing includes addressing on a frame-by-frame basis the video signal being
3 post-processed.

4
5 44. (Currently Amended) A video output system for producing video signals
6 within a video graphics workstation, the video output system comprising:

7 means for receiving a video signal forwarded from a video signal source within
8 the video graphics workstation;

9 means for post-processing the received video signal through a video pipeline,
10 producing a post-processed video signal; and

11 means for converting the post-processed video signal, producing a formatted
12 video signal.

13
14 45. (Original) The system according to claim 44, further comprising:

15 means for inserting ancillary data into the post-processed video signal prior to
16 converting the post-processed video signal.

17
18 46. (New) The video output system according to claim 1 wherein the receiver and
19 the video pipeline are implemented as an integrated video processing module, and
20 wherein the video output module is detachably coupled to the video processing module.

21
22 47. (New) The video output system according to claim 46 wherein the video
23 output module is a daughterboard module that couples to the video processing module.
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1 48. (New) The video output system according to claim 46 wherein the video
2 output module includes a processor that is configured to inform the video processing
3 module of its configuration.

4
5 49. (New) The method according to claim 23 wherein the receiver and the video
6 pipeline are implemented as an integrated video processing module, and wherein the
7 method further includes detachably coupling the video output module to the video
8 processing module.

9
10 50. (New) The method according to claim 49 wherein the video output module is
11 a daughterboard module that couples to the video processing module.

12
13 51. (New) The method according to claim 49 wherein the video output module
14 includes a processor, and wherein the processor informs the video processing module of
15 its configuration.

16
17 52. (New) A video output system for producing video signals, the video output
18 system being coupled to a video graphics processor, a video signal input system, and a
19 storage medium for storing data in electrical form, the video output system comprising:

20 a receiver for receiving a video signal;

21 a video pipeline for post-processing the received video signal, the video pipeline
22 producing a post-processed video signal; and

23 a video output module for converting the post-processed video signal, the video
24 output module producing a formatted video signal,

25 wherein the received video signal is selectively forwarded from:

1 the storage medium;
2 the video graphics processor; or
3 the video signal input system.

4
5 53. (New) The video output system according to claim 52 wherein the video
6 pipeline is configured to perform plural functions selected from the following functions:

7 region of interest selection;
8 frame rate matching;
9 spatial adaptation;
10 changing the sample rate of the video signal being post-processed;
11 gamma removal;
12 gamma insertion;
13 color space conversion;
14 changing frames of video data into interleaved fields of video data; and
15 addressing on a frame-by-frame basis the video signal being
16 post-processed.

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18 54. (New) The video output system according to claim 53 wherein the video
19 pipeline includes functionality for performing each said function.

20
21 55. (New) The video output system according to claim 53 wherein the video
22 output module further comprises at least one of:

23 an ancillary data injector, the injector inserting ancillary data into the
24 post-processed video signal; and
25 a generator locking device.

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2 56. (New) The video output system according to claim 54 wherein the video
3 output module further comprises:

4 an ancillary data injector, the injector inserting ancillary data into the
5 post-processed video signal; and
6 a generator locking device.

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8 57. (New) A method for producing video signals using a video output system, the
9 video output system being coupled to a video graphics processor, a video signal input
10 system, and a storage medium for storing data in electrical form, the method comprising:

11 receiving a video signal in a receiver of the video output system;

12 post-processing the received video signal through a video pipeline of the video
13 output system, producing a post-processed video signal; and

14 converting the post-processed video signal in a video output module of the video
15 output system, producing a formatted video signal,

16 wherein the received video signal is selectively forwarded from:

17 the storage medium;

18 the video graphics processor; or

19 the video signal input system.

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21 58. (New) The method according to claim 57 wherein the video pipeline performs
22 plural functions selected from the following functions:

23 region of interest selection;

24 frame rate matching;

25 spatial adaptation;

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changing the sample rate of the video signal being post-processed;
gamma removal;
gamma insertion;
color space conversion;
changing frames of video data into interleaved fields of video data; and
addressing on a frame-by-frame basis the video signal being
post-processed.

59. (New) The method according to claim 58 wherein the video pipeline includes
functionality for performing each said function.

60. (New) The method according to claim 58, further comprising performing at
least one of:

inserting ancillary data into the post-processed video signal; and
generator locking the receive video signal.

61. (New) The method according to claim 59, further comprising:
inserting ancillary data into the post-processed video signal; and
generator locking the receive video signal.